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22. (Amended) The method of claim 16, further comprising the step of enabling a clear memory mode when the count is fifty, the predetermined value of activation time period is one half second, and the defined period of time is within three hundred milliseconds of another activation.

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**REMARKS**

In the Ex Parte Quattle Action mailed on July 23, 2002, the examiner requested a compact disk containing a computer program listing in accordance with 37 CFR 1.96. The CD is submitted in duplicate along with a transmittal cover letter. The specification is amended incorporating reference to the computer program listing appendix, and updating references to the parent applications.

Upon entry of the present amendment, the specification and claims 12-16 and 18-22 have been amended in accordance with the Examiner's suggestions without adding new matter. Applicants note with appreciation the Examiner's assistance in this matter. The amendments were made to more particularly point out the subject matter which the applicants regard as their invention.

All claims are allowable, and the application is now in condition for allowance.

The Commissioner is hereby authorized to charge any additional fees which may be required in the application to Deposit Account No. 06-1135.

Respectfully submitted,

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**A MARKED-UP COPY SHOWING AMENDMENTS  
IN RESPONSE TO EX PARTE QUAYLE ACTION**

**Please amend paragraph on page 9, beginning with line 25 as follows:**

--Outer sleeve 142 is also fixedly attached to a ring 136. Ring 136 drives position detector assembly 124. Position detector assembly 124 is electrically coupled to a control board 144. Control board 144 contains the electronics for starting and controlling the motor 130 (see Fig. 6). A capacitor 126 is used to start motor 130 (described below). A brake 128 is provided to slow motor 130 when the rolling shutter is approaching a limit position. Position detector assembly 124 may be a pass point assembly as described in [application no. (attorney docket 64234)] U.S. Patent No. 6,133,703 assigned to the assignee of this application or an absolute position detector assembly as described in application [no. (attorney docket 65468) assigned to the assignee of this application] Serial No. 09/251,307, now abandoned. --

12. (Amended) The movable barrier operator of claim 9, wherein the controller, responsive to seven consecutive activations of one of the input devices, wherein each activation is within three hundred milliseconds of another activation and is for a delay less than one half second, for enabling a reset mode.

13. (Amended) The movable barrier operator of claim 9, wherein the controller, responsive to five consecutive activations of one of the input devices, wherein each activation is within three hundred milliseconds of another activation and is for a delay less than one half second, for enabling a group control mode.

14. (Amended) The movable barrier operator of claim 9, wherein the controller, responsive to twenty consecutive activations of one of the input devices, wherein each activation is within three hundred milliseconds of another activation and is for a delay less than one half second, for enabling a lock mode.

15. (Amended) The movable barrier operator of claim 9, wherein the controller, responsive to fifty consecutive activations of one of the input devices, wherein each activation is within three hundred milliseconds of another activation and is for a delay less than one half second, for enabling a clear memory mode.

16. (Amended) A method of programming a controller for a movable barrier operator, comprising:

detecting activation of an input device;  
measuring the period of time of the activation of the input device;  
changing a count if the measured activation time period is less than a predetermined value and within a defined period of time;  
enabling a learn mode when the count on a counter is equal to a predetermined value;  
and  
activating a motor to move the barrier if the measured period of time is greater than the predetermined value.

18. (Amended) The method of claim 17, wherein the predetermined value of the count is seven, the predetermined value of activation time period is one half second, and the defined period of time is within three hundred milliseconds of another activation.

19. (Amended) The method of claim 16, further comprising the step of enabling a reset mode when the count is seven, the predetermined value of activation time period is one half second, and the defined period of time is within three hundred milliseconds of another activation.

20. (Amended) The method of claim 16, further comprising the step of enabling a group control mode when the count is five, the predetermined value of activation time period is one half second, and the defined period of time is within three hundred milliseconds of another activation.

21. (Amended) The method of claim 16, further comprising the step of enabling a lock mode when the count is twenty, the predetermined value of activation time period is one half second, and the defined period of time is within three hundred milliseconds of another activation.

22. (Amended) The method of claim 16, further comprising the step of enabling a clear memory mode when the count is fifty, the predetermined value of activation time period is one half second, and the defined period of time is within three hundred milliseconds of another activation.